MH3
1
MTG_Pads

MH1
1
MTG_Pads

MH2
1
MTG_Pads

PCB Number: MD106
PCB Rev: A

PCB
LOGO
Texas Instruments

CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo



CAUTION HOT SURFACE



DANGER HIGH VOLTAGE

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

Variant/Label Table	
Variant	Label Text
001	DRV8374S-29EVM
002	DRV8378S-29EVM

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

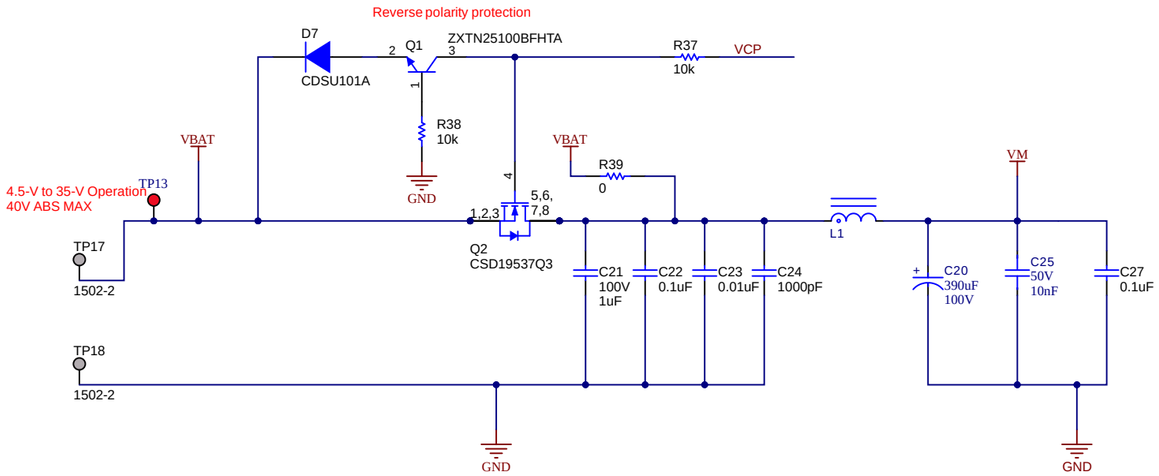
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Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 4/16/2026
TID #: N/A		Project Title: DRV8374EVM	
Number: MD106	Rev: A	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 3
Drawn By:		File: Sheet3.SchDoc	Size: B
Engineer: Yara Al-Shami		Contact: http://www.ti.com/support	

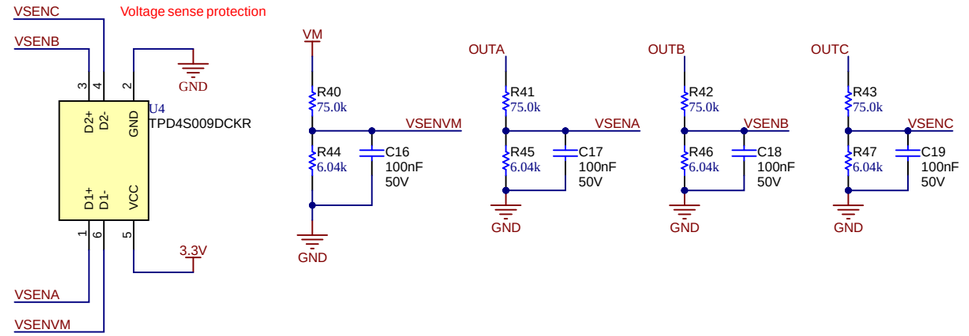
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MAIN SUPPLY



VOLTAGE SENSE & PROTECTION



CONNECTORS & INTERFACE

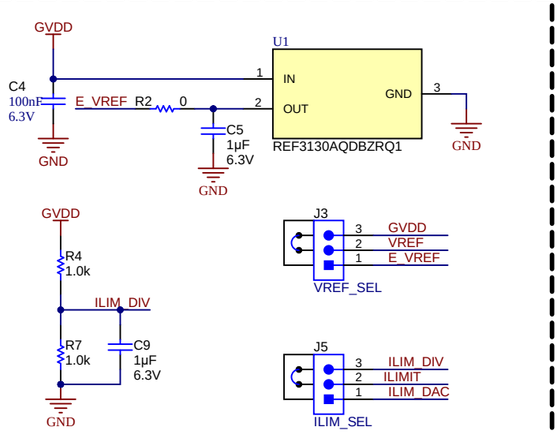
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Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 4/16/2026
TID #: N/A		Project Title: DRV8374EVM	
Number: MD106	Rev: A	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 3
Drawn By:		File: Sheet2.SchDoc	Size: B
Engineer: Yara Al-Shami		Contact: http://www.ti.com/support	

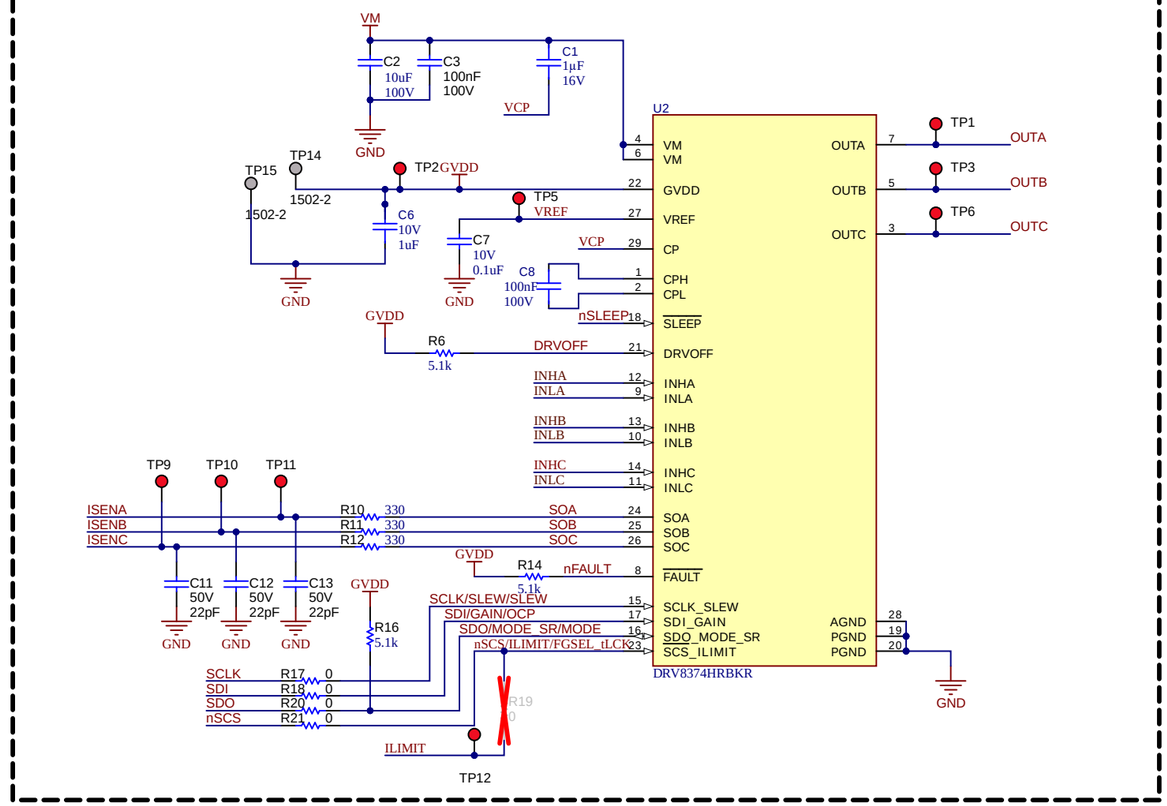
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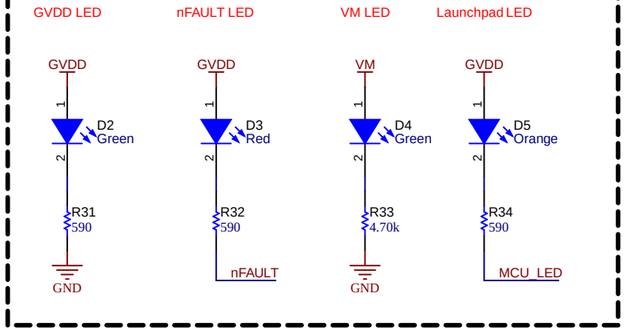
VREF / ILIM



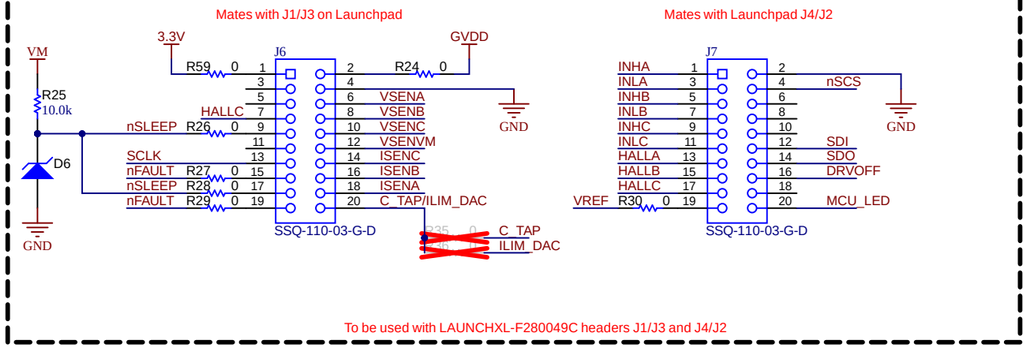
DRV8374X



Status LEDs



LAUNCHPAD CONNECTIONS



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Orderable: ChangeMe in variant		Designed for: Public Release	Mod. Date: 4/15/2026
TID #: N/A		Project Title: DRV8374EVM	
Number: MD106	Rev: A	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 2 of 3
Drawn By:		File: Sheet1.SchDoc	Size: B
Engineer: Yara Al-Shami		Contact: http://www.ti.com/support	

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